

## Linear Systems replaces discontinued Siliconix J210

The J210 is a n-channel JFET General Purpose amplifier with low noise and low leakage.

The SOT-23 package is well suited for cost sensitive applications and mass production.

(See Packaging Information).

**J210 Benefits:**

- High gain
- Low Leakage
- Low Noise

**J210 Applications:**

- General Purpose Amplifiers
- UHV / VHF Amplifiers
- Mixers
- Oscillators

FEATURES	
DIRECT REPLACEMENT FOR SILICONIX J210	
HIGH GAIN	$g_{fs} = 7000\mu\text{mho MIN}$
HIGH INPUT IMPEDANCE	$I_{GSS} = 100\text{pA max}$
LOW INPUT CAPACITANCE	$C_{iss} = 5\text{pF}$
<b>ABSOLUTE MAXIMUM RATINGS @ 25°C (unless otherwise noted)</b>	
<b>Maximum Temperatures</b>	
Storage Temperature	-55°C to +150°C
Operating Junction Temperature	-55°C to +135°C
<b>Maximum Power Dissipation</b>	
Continuous Power Dissipation	360mW
Derating over temperature	3.27 mW/°C
<b>MAXIMUM CURRENT</b>	
Gate Current (Note 1)	10mA
<b>MAXIMUM VOLTAGES</b>	
Gate to Drain Voltage or Gate to Source Voltage	-25V

### J210 ELECTRICAL CHARACTERISTICS @ 25°C (unless otherwise noted)

SYMBOL	CHARACTERISTIC	MIN	TYP.	MAX	UNITS	CONDITIONS
$BV_{GSS}$	Gate to Source Breakdown Voltage	-25	--	--	V	$V_{DS} = 0V, I_G = -1\mu\text{A}$
$V_{GS(off)}$	Gate to Source Cutoff Voltage	-1	--	-3	V	$V_{DS} = 15V, I_D = 1\text{nA}$
$I_{DSS}$	Drain to Source Saturation Current (Note 2)	2	--	15	mA	$V_{DS} = 15V, V_{GS} = 0V$
$I_{GSS}$	Gate Reverse Current (Note 3)	--	--	-100	pA	$V_{DS} = 0V, V_{GS} = -15V$
$I_G$	Gate Operating Current (Note 3)	--	-10	--	pA	$V_{DS} = 10V, I_D = 1\text{mA}$
$r_{DS(on)}$	Drain to Source On Resistance	--	--	50	$\Omega$	$I_G = 1\text{mA}, V_{DS} = 0V$

### J210 DYNAMIC ELECTRICAL CHARACTERISTICS @ 25°C (unless otherwise noted)

SYMBOL	CHARACTERISTIC	MIN	TYP.	MAX	UNITS	CONDITIONS
$g_{fs}$	Forward Transconductance	4000	--	12000	$\mu\text{mho}$	$V_{DS} = 15V, V_{GS} = 0V, f = 1\text{kHz}$
$g_{os}$	Output Conductance	--	--	150	$\mu\text{mho}$	
$C_{iss}$	Input Capacitance	--	4	--	pF	$V_{DS} = 15V, V_{GS} = 0V, f = 1\text{MHz}$
$C_{rss}$	Reverse Transfer Capacitance	--	1	--	pF	
$e_n$	Equivalent Noise Voltage	--	10	--	nV/√Hz	$V_{DS} = 15V, V_{GS} = 0V, f = 1\text{kHz}$

### J210 SWITCHING CHARACTERISTICS @ 25°C (unless otherwise noted)

SYMBOL	CHARACTERISTIC	UNITS	CONDITIONS
$t_{d(on)}$	Turn On Time	2	$V_{DD} = 10V$ $V_{GS(H)} = 0V$ See Switching Circuit
$t_r$	Turn On Rise Time	2	
$t_{d(off)}$	Turn Off Time	6	
$t_f$	Turn Off Fall Time	15	

Note 1 - Absolute maximum ratings are limiting values above which J210 serviceability may be impaired.

Note 2 - Pulse test duration = 2ms

Note 3 - Approximately doubles for every 10°C increase in  $T_A$

Micross Components Europe

Available Packages:

SOT-23 (Top View)



J210 in SOT-23  
J210 in bare die.

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Please contact Micross for full package and die dimensions

